

## **APPENDIX**

1. (Amended) A spindle motor control circuit for controlling a motor;  
comprising;  
a control circuit to control said motor during at least a low voltage state, a pulse voltage state and a high voltage state;  
said motor braking during said low voltage state;  
said control circuit receiving a flyback voltage from said motor during said pulse voltage state;  
said control circuit receiving a reduced flyback voltage being reduced from said flyback voltage from said motor during said high voltage state.
2. (Amended) A spindle motor control circuit for controlling a motor, as in Claim 1, wherein said control circuit includes an op amp to feed back a voltage to limit said flyback voltage from said motor.
3. (Amended) A spindle motor control circuit for controlling a motor, as in Claim 1, wherein said voltage is a first voltage during said pulse voltage state and a second voltage during said high voltage state.
4. (Original) A spindle motor control circuit for controlling a motor, as in Claim 3, wherein said first voltage is greater than said second voltage.
5. (Amended) A spindle motor control circuit for controlling a motor, as in Claim 1, wherein said motor is braked before said pulse voltage state and after said high voltage state.